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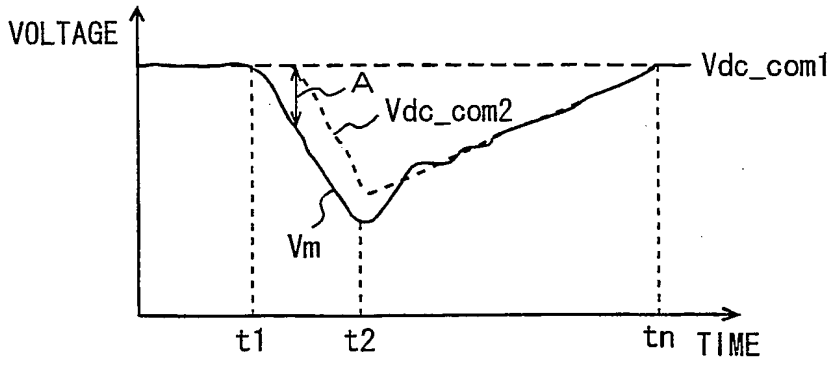
(54) Title: VOLTAGE CONVERSION DEVICE AND COMPUTER-READABLE RECORDING MEDIUM HAVING PROGRAM RECORDED THEREON FOR COMPUTER TO CONTROL VOLTAGE CONVERSION



(57) Abstract: In a case where the deviation between a target voltage (Vdc_com1) and an output voltage (Vm) is larger than a specified value (A), a voltage command (Vdc_com2) of a voltage step-up converter (12) is calculated by adding the specified value (A) to the output voltage (Vm). When the output voltage (Vm) having been decreasing starts to increase, the voltage command (Vdc_com2) is calculated in such a manner that the rate of change of the voltage command (Vdc_com2) is equal to or smaller than a standard value. Using the calculated voltage command (Vdc_com2), feedback control of the voltage step-up converter is conducted in such a manner that the output voltage (Vm) is equal to the target voltage (Vdc_com1).

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